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Docket No.: 0756-2433

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

) Examiner: Stanetta Isaac

Shunpei YAMAZAKI et al.

) Group Art Unit: 2812

Serial No. 10/072,931

Filed: February 12, 2002

For: METHOD OF MANUFACTURING

A SEMICONDUCTOR DEVICE

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**REQUEST FOR ACKNOWLEDGMENT OF
INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

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Applicants acknowledge with appreciation the Notice of Allowability and Issue Fee Due form mailed June 12, 2002. The Notice of Allowability did not, however, include an initialed Form PTO-1449 acknowledging the Information Disclosure Statement filed February 12, 2002. Therefore, it is respectfully requested that the Examiner evidence consideration of the references cited in the IDS of February 12, 2002 by providing a copy of the initialed Form PTO-1449.

If a conference would be beneficial in expediting the prosecution of the instant application, the Examiner is invited to telephone the undersigned to arrange such a conference.

Respectfully submitted,

Eric J. Robinson
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Rose M. Fichtel

(Depositor's name)

Rose M Fichtel

(Signature)

September 12, 2002

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/072,931	02/12/2002	Shunpei Yamazaki	740756-2433	3751

TITLE OF INVENTION: METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1280	\$300	\$1580	09/12/2002

EXAMINER	ART UNIT	CLASS-SUBCLASS
ISAAC, STANETTA D	2812	438-166000

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Semiconductor Energy Laboratory Co., Ltd.

Kanagawa-ken, Japan

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